

GP 2682
ZhIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the name of:

Date: March 14, 2001

Cromer, et al.

Serial No.: 09/282,893

Group Art Unit: 2682

Filing Date: March 31, 1999

Examiner: Banks-Harold, M.

Title: METHOD AND SYSTEM FOR PROVIDING PROTECTION AGAINST
THEFT AND LOSS OF A PORTABLE COMPUTER SYSTEMCommissioner of Patents and Trademarks
Washington, D.C. 20231**RECEIVED**

MAR 22 2001

Technology Center 2600

TRANSMITTAL LETTER

Sir:

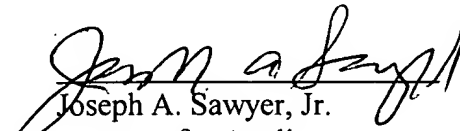
In response to the Office Action dated December 21, 2000, please find enclosed the following:

- 1) Amendment responsive to the Office Action dated December 21, 2000
- 2) Transmittal Letter (original and two copies)
- 3) Postcard
- 4) The fee for claims has been calculated as shown below:

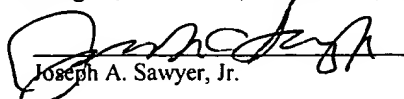
<input checked="" type="checkbox"/> Large Entity <input type="checkbox"/> Small Entity					
Fee Items	Claims remaining after Amendment	Highest # of Claims previously paid for	Extra Claims	Fee Rate	TOTAL
Total Claims	17	- 17 =	0	x 18.00	\$ 0.00
Independent	3	- 3 =	0	x 80.00	\$ 0.00
Multiple Dependent Claim fee (one or more, first added by this amendment)				270.00	\$ 0.00
Total Fees					\$ 0.00

If any unresolved issues remain, please contact Applicant's attorney at the telephone number below.

Respectfully submitted,


Joseph A. Sawyer, Jr.
Attorney for Applicants
Reg. No. 30,801
(650) 493-4540

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail and is addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231, on March 14, 2001.


Joseph A. Sawyer, Jr.



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: March 14, 2001

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For: METHOD AND SYSTEM FOR PROVIDING PROTECTION AGAINST
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Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

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AMENDMENT

Sir:

In response to the Office Action dated December 21, 2000, please amend the above-identified application as follows:

In the Specification:

Please replace the paragraph beginning at page 4, line 3, with the following rewritten paragraph:

PCI bridge controller 212 includes an interface for a flash memory 242 which includes microcode which executes upon power-on. Flash memory 242 is an electrically erasable programmable read only memory (EEPROM) module and includes BIOS (basic input/output system) that is used to interface between the I/O devices and operating system. PCI bridge

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner of Patents and Trademarks, Washington, D.C. 20231, on March 14, 2001.

Joseph A. Sawyer, Jr.